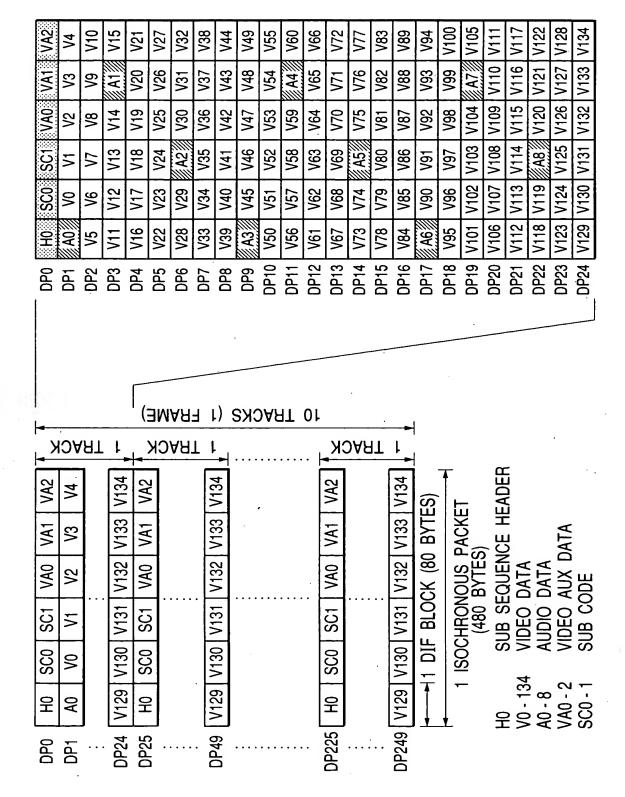
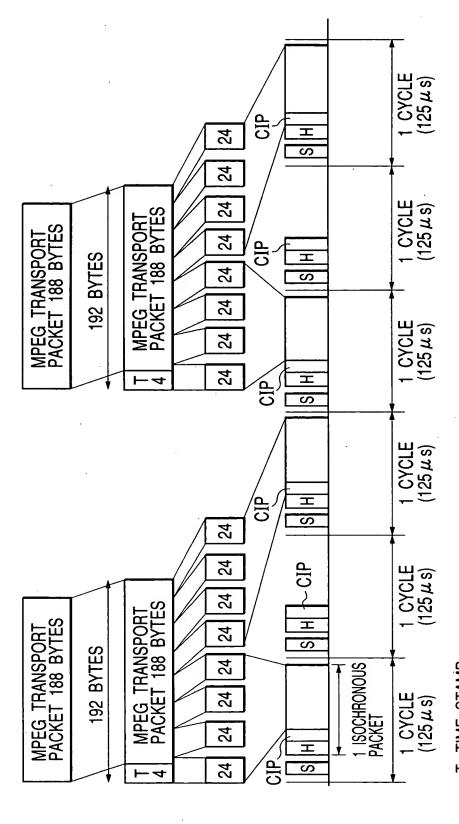


FIG. 4







T: TIME STAMP

S: CYCLE START PACKET H: ISOCHRONOUS PACKET HEADER